

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

TANIGUCHI

Application No.: 09/777,897

Filed: February 7, 2001



Art Unit: 2816

Examiner: L. Nguyen

Atty. Dkt. No.: 100353-00039

RECEIVED
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TECHNICAL CENTER 2800

For: DELAY TIME ADJUSTING METHOD OF DELAYING A PHASE OF AN OUTPUT SIGNAL UNTIL A PHASE DIFFERENCE BETWEEN AN INPUT SIGNAL AND THE OUTPUT SIGNAL BECOMES AN INTEGRAL NUMBER OF PERIODS OTHER THAN ZERO

#6/A

Amend

Jan Miller

1/12/02

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

January 7, 2002

Sir:

In reply to the Office Action mailed September 6, 2001, the period for response being extended to January 7, 2002 (January 6, 2002 being a Sunday) by the attached petition for Extension of Time, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Please amend the specification as follows:

Please replace page 11, paragraph 5, starting at line 22 with:

It should be noted that the frequency divider 2 may be considered to be an element that determines a target used in adjusting a phase of a signal. The second frequency divider 4 may be considered to be an element that determines how frequently the phase of a signal has a chance to be adjusted.

A1

IN THE DRAWINGS:

Please amend Fig. 5 as shown in the attached Request for Approval of Drawing Corrections.